

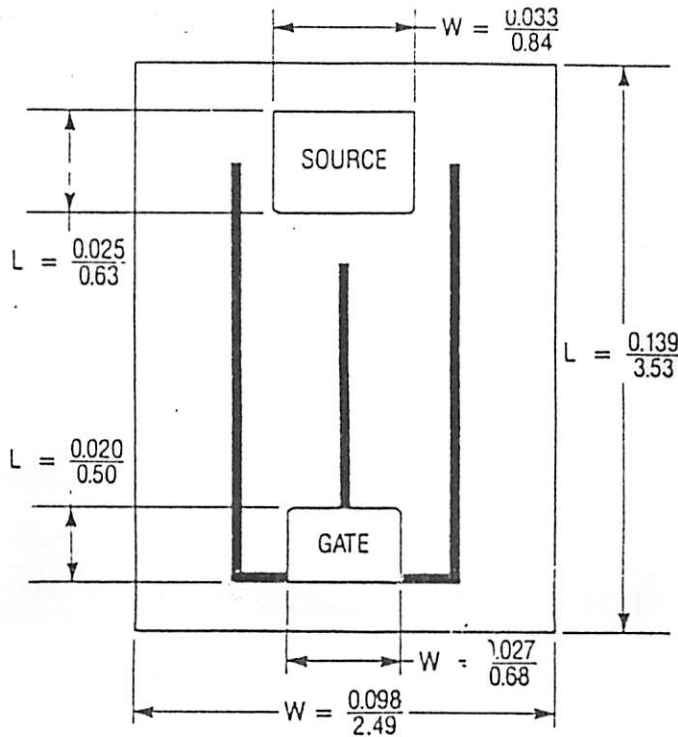


# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below.



**Topside Metal: Aluminum**  
**Backside Metal: CrNiAg**  
**Backside Potential: Drain**  
**Mask Ref: Issue 1**  
**Bond Pads (Mils): .004 Min.**

**APPROVED BY:**

**MFG: IRI**

**DIE SIZE: .098" X .139"**

**THICKNESS: .015"**

**DATE: 3/21/00**

**P/N: IRF820**

DG 10.1.2

Rev A 3-4-99